

# CPRI / OBSAI

Měření na základnových stanicích s RRH





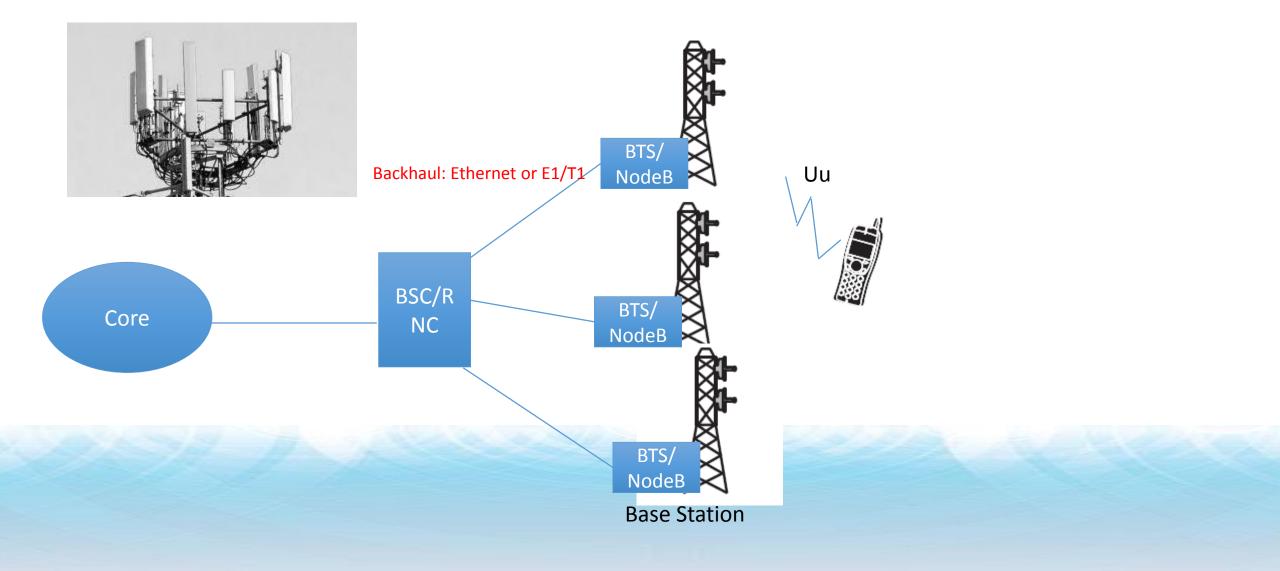


**Radio Access Network Evolution CPRI/OBSAI** Technologies Detailed Look at the CPRI Protocol **RAN Testing Challenges** Unframed CPRI/OBSAI Testing with TX300S/320S – User guide Layer 1 Framed CPRI Testing with TX300S/320S – User guide Layer 2 CPRI Testing with TX300S/320S – User guide Layer 2 CPRI Monitoring with TX320S – User guide





#### Tradiční RAN architektura





#### BTS a RF část v kolokaci

Electrical cable
Base Station/Node
B

In traditional deployments, the base station functions are co-located with the radio tower at the base of the antenna or basement of a tall building.

Drawbacks of co-location:

RF transceiver and power amplifier needed to drive the radio functions are co-located with the other functions of the base station such as backhaul transport, control and baseband processing.

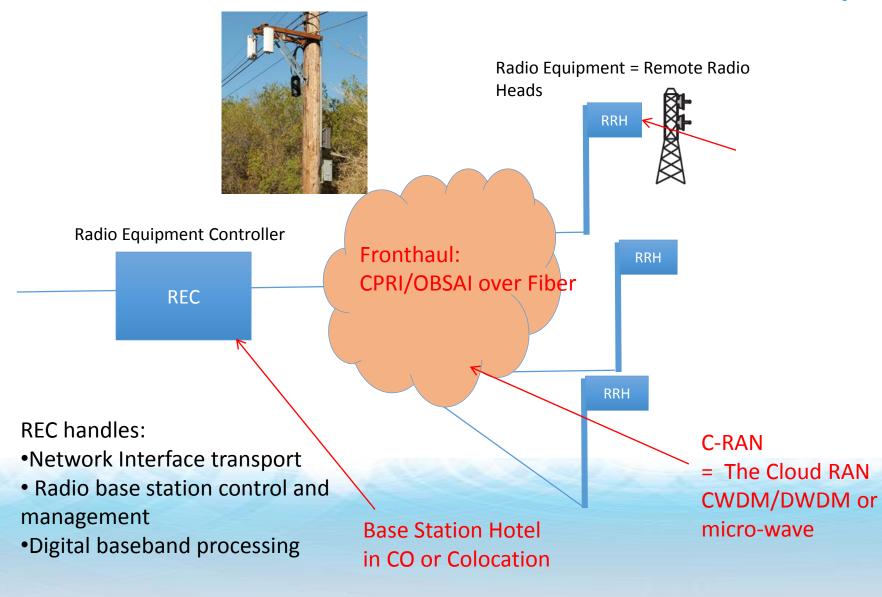
Needs to be physically located very close to the antenna because they are driven over very lossy electrical cables.

High power dissipation requires that require costly special enclosures with climate control and power redundancy

Makes it difficult to install and find a location that can host the enclosure and/or increase lease price due to additional space needed

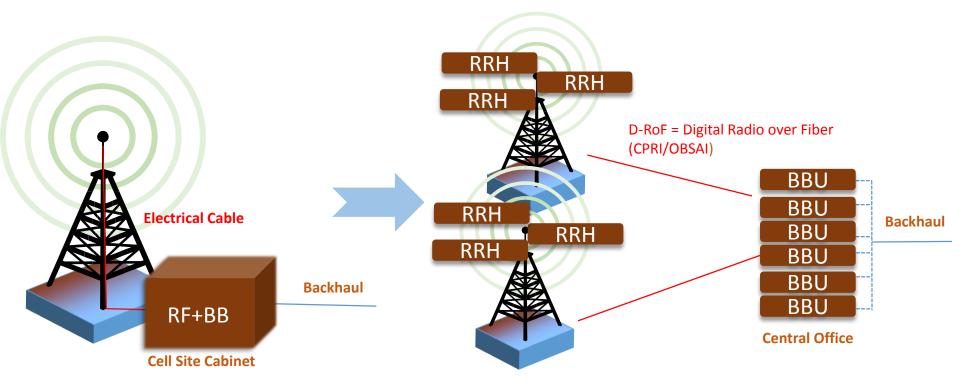
# Distribuované anténní systémy





# Migrace k C-RAN





#### **Benefits:**

Separation of RF and Baseband processing. Simplified function makes it more compact, easier to install and therefore increases the number of possible sites Fiber Link replaces COAX, less power required at the cell site Optimized X2 interface allows for LTE-A features support

#### CPRI vers. OBSAI



CPRI and OBSAI are two competing standards

CPRI stands for Common Public Radio Interface, this protocol has been developed by Ericsson AB, Huawei Technologies Co. Ltd, NEC Corporation, Alcatel Lucent and Nokia Siemens

OBSAI stands for Open Base Station Architecture Initiative, this protocol has been developed by Hyundai, LGE, Nokia, Samsung and ZTE

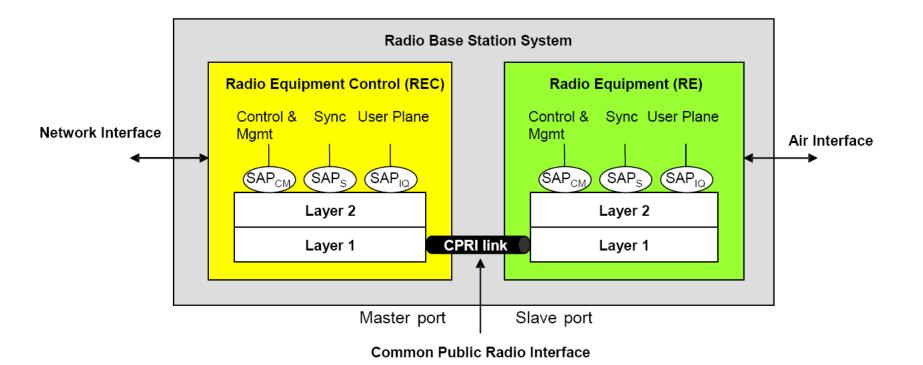
CPRI Rates range from 614 Mbps to 10.137 Gbps

OBSAI Rates range from 728 Mbps to 6.8 Gbps

CPRI has higher market penetration rate than OBSAI

#### **CPRI-** specifikace





Specification written with the goal to be generic enough to support scalable rates, physical access medium type and air interfaceCPRI specification defines only Layer 1 and Layer 2 technologies

# CPRI- bitové rychlosti



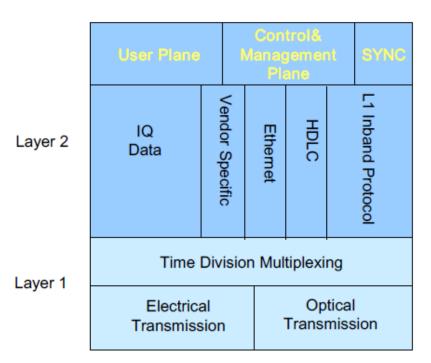
The following Line bit rates are defined in the standard:

- CPRI line bit rate option 1: 614.4 Mbit/s
- CPRI line bit rate option 2: 1228.8 Mbit/s (2 x 614.4 Mbit/s)
- CPRI line bit rate option 3: 2457.6 Mbit/s (4 x 614.4 Mbit/s)
- CPRI line bit rate option 4: 3072.0 Mbit/s (5 x 614.4 Mbit/s)
- CPRI line bit rate option 5: 4915.2 Mbit/s (8 x 614.4 Mbit/s)
- CPRI line bit rate option 6: 6144.0 Mbit/s (10 x 614.4 Mbit/s)
- CPRI line bit rate option 7: 9830.4 Mbit/s (16 x 614.4 Mbit/s)
- CPRI line bit rate option 8: 10137.6 Mbit/s, 64B/66B line coding (20 x 491.52 x 66/64 Mbit/s)

Rates chosen to be integer multiples of UMTS chip rate at 3.84 M

# CPRI- protokol





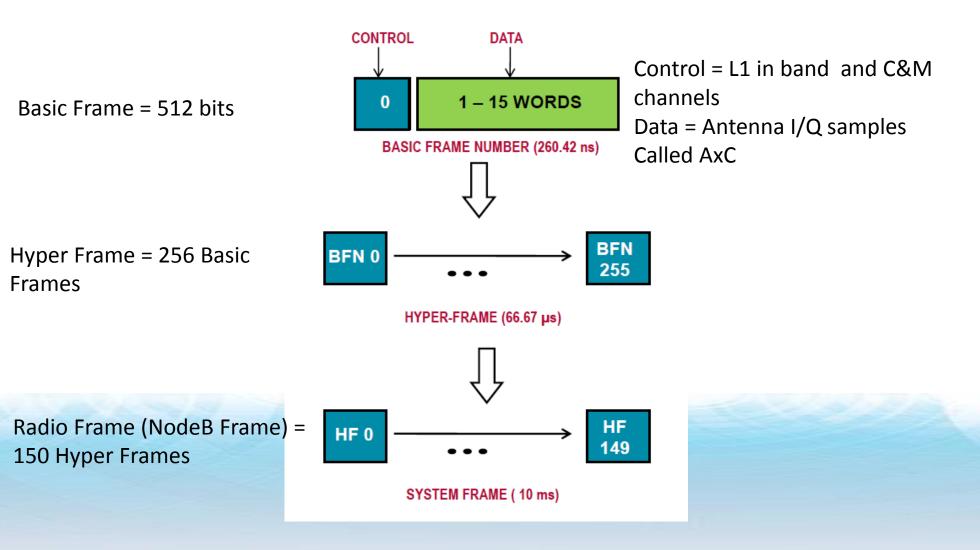
**I/Q data**: User plane information in the form of in-phase and quadrature modulation data (digital baseband signals)

**L1 inband protocol:** Signalling information that is related to the link and is directly transported by the physical layer.

**C&M data:** Control and management information exchanged between the C&M entities within the REC and the RE. This information flow is given to the higher protocol layers. Two different layer 2 protocols for C&M data are supported: subset of High level Data Link Control (HDLC) and Ethernet.

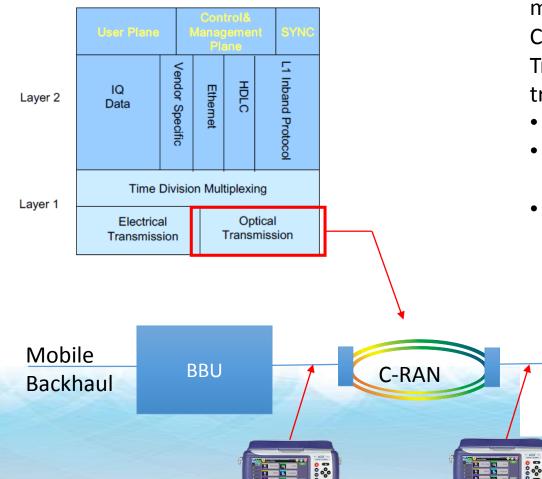
#### CPRI- struktura rámce





## CPRI- měřicí úlohy





The Cloud RAN (C-RAN) can be dark fiber, DWDM/CWDM, micro-wave or CPRI over OTN with Fronthaul distance up to 40km. Transport should be tested for error free CPRI transmission, the TX300S validates:

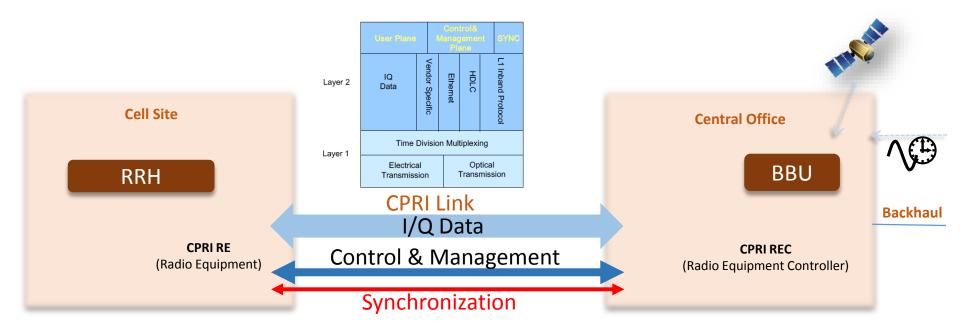
• Optical power

RRH

- BERT with PRBS pattern CPRI standard requirement of 10<sup>-12</sup> BER
- Accurate Round trip delay measurement



#### Fronthaul a synchronizace



CPRI delivers synchronization to the RRH. No GPS antenna or other sync solution needed at the RRH RRH extracts its clock from the CPRI bit stream BBU centrally located has available choices to get sync including 1588 PTP delivered via backhaul or GPS signal



# Synchronizace a zpoždění

CPRI is a Synchronous technology, similar to SyncE in Clock recovery. The CPRI Slave (RE/RRH) recovers clock from CPRI link.

• TX300S Master or REC provides stable clock traceable to PRC external ref clock. TX300S Slave or RE recovers clock and verifies timing recovery

CPRI has strict timing requirements, Remote Radio Heads have to be synchronized to Controller in order to avoid dropped calls.

 TX300S supports CPRI standard compliant Round trip delay measurement mechanism at Layer 2







### Emulace provozu na L2

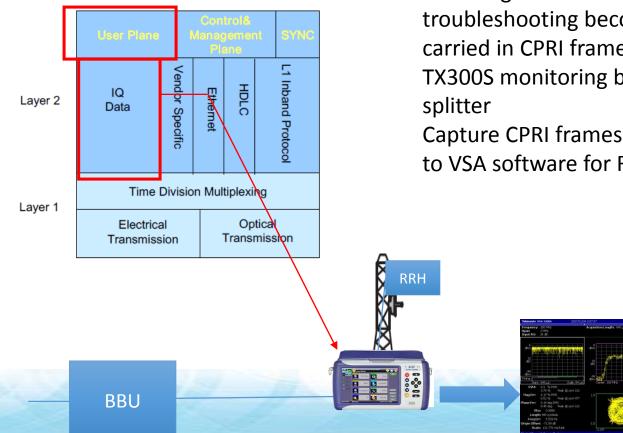


- Test Network with Performance Statistics with TX300S REC/BBU Master Emulation to TX300S RE/RRH Slave Emulation.
- View Alarms: LOS, LOF, RAI, SDI
- Code violations, BER Statistics
- Service Disruption Testing (SDT)
- Frame Counters (HFN and BFN)

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# Analýza RF parametrů



No RF signal availability at the bottom of the tower – RF troubleshooting becomes difficult. Only digital RF signal carried in CPRI frame.

TX300S monitoring between BBU and RRH via optical splitter

Capture CPRI frames I/Q data and export them in csv format to VSA software for RF analysis



#### C&M parametry



Troubleshoot interoperability issues with Control Words Protocol Decode.

- Decode Sync and Timing Bytes
- Verify proper incrementing of BFN and HFN frame counters
- L1 Inband Protocol decode
- HDLC and Ethernet C&M Bytes decode
- Bi-directional dual port in-service monitoring

6.144G	<b>1</b> 6.	144G	RUN	•	*				3			
LEDs	0-15		16-31					32-47	48-63			
	0	Sync & timing	SYN	HFN	BFN	BFN	8	Reserved	RES	RES	RES	RES
Signal			вс	55	E8	09			00	00	00	00
	1	Slow C&M	C&M		C&M		9	Reserved	RES	RES		RES
Frame			7E	7E	7E	7E			00	00	00	00
	2	L1 inband prot.	VER	STR	L1	Ptr	10	Reserved	RES	RES	RES	RES
Pattern			01	04	00	14			00	00	00	00
	3	Reserved	RES		RES	RES	11	Reserved	RES	RES		RES
<b>•</b>			00	00	00	00			00	00	00	00
ALM/ERR	4	Ctrl_AxC low	Ctl	Ctl	Ctl	Ctl	12	Reserved	RES	RES	RES	RES
		Byte	00	00	00	00			00	00	00	00
History	5	Ctrl_AxC low	Ctl	Ctl	Ctl	Ctl	13	Reserved	RES	RES		
		Byte	00	00	00	00			00	00	00	00
	6	Ctrl_AxC high	Ctl	Ctl	Ctl	Ctl	14	Reserved	RES	RES		RES
		Byte	00	00	00	00			00	00	00	00
RX: 6.144G	7	Ctrl_AxC high	Ctl	Ctl	Ctl	Ctl	15	Reserved	RES	RES	RES	RES
TX: 6.144G		Byte	00	00	00	00			00	00	00	00
(P) 192.168.0.148	R Re	mote/CLI						2014-05-30 03:59:25				

Ξ

Inband Protocol

Vendor Specific

IQ

Data

Electrical

Transmission

Layer 2

Layer 1

Ethernet

Time Division Multiplexing

HDLC

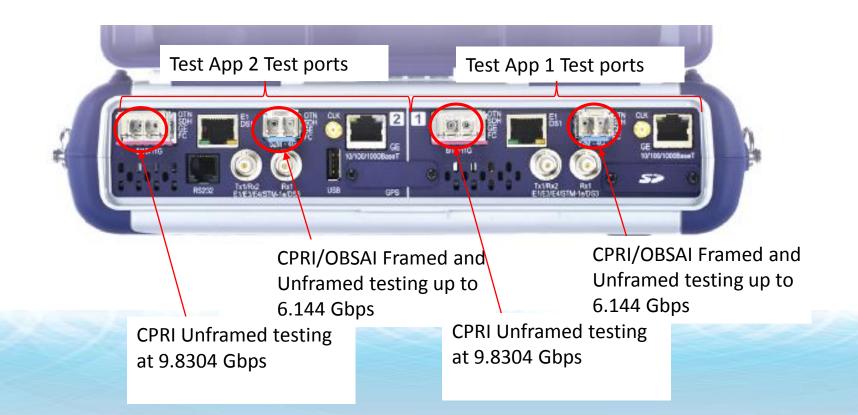
Optical Transmission



#### Měření pomocí TX300S

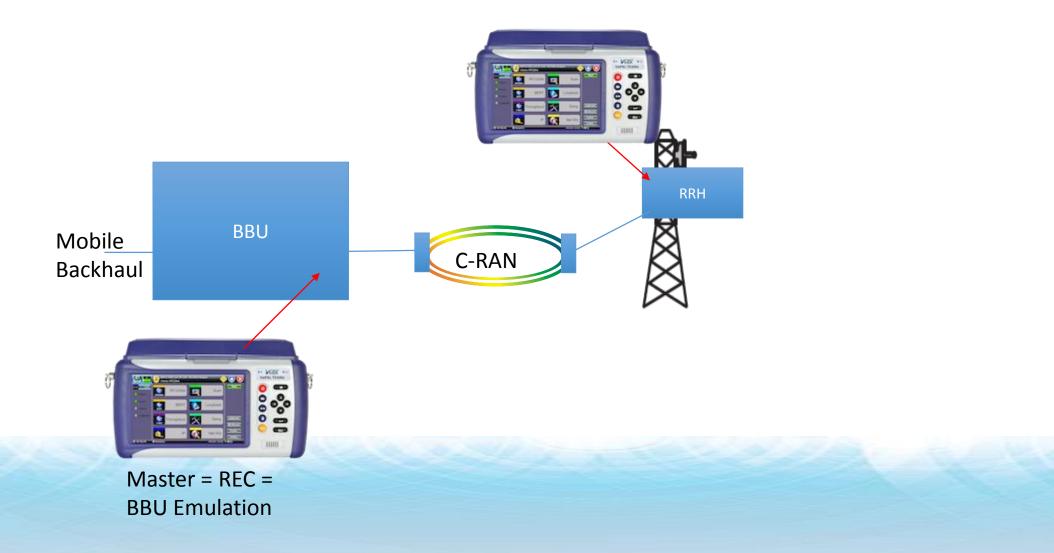


Note: For CPRI and OBSAI rates of 4.9152 and 6.144 Gbps an SFP+ should be used.



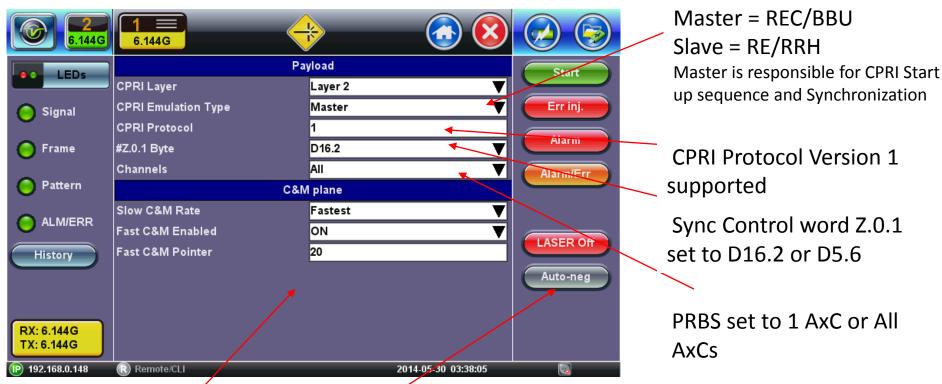
#### RRC/RE L2 měření





# CPRI konfigurace pro L2 měření

elektronické měřicí přístroje



Control and Management channel configuration

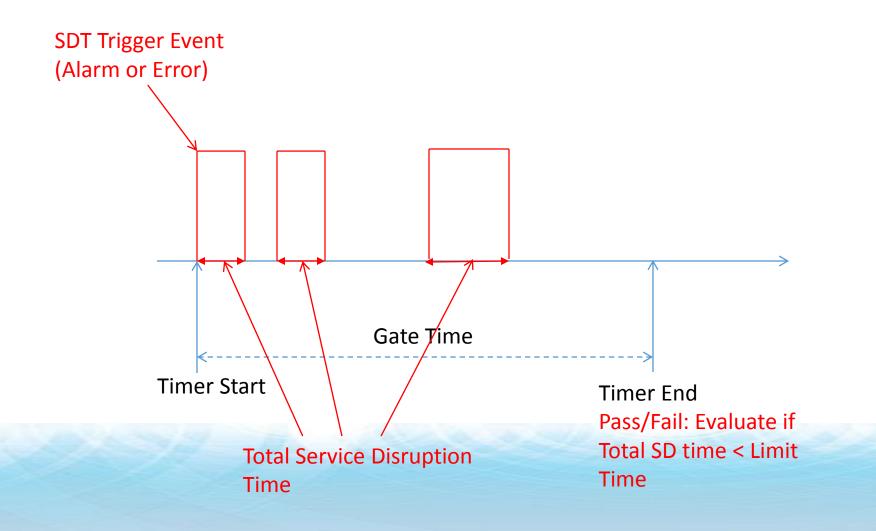
Slow C&M = HDLC channel, configurable rate or disable channel

Fast C&M = Ethernet channel, configurable start of Ethernet channel pointer in Control word or channel disabled.

Auto-negotiation can be used for Master/Slave to negotiate their maximum C&M channels capabilities



#### Mechanismus SDT měření



# Výsledky SDT měření



Limit and Gate Time counters begin at the onset of the first valid event.

SDT Measurement ends after the Gate time is elapsed, to allow the capture of multiple smaller events.

The total time from the beginning of the first event to the end of the last event (within the Gate Time) is the reported SD time.

The measurement process is immediately restarted in search for the next trigger. Results are presented in tabular form (Events table) indicating SD start time (1 ms resolution or better), disruption time, and Pass/Fail evaluation. This table gets populated as new disruptions are detected and measured.

6.144G	<b>1</b> 6.144G			<b>(3)</b>		6.144G	<b>1</b> 6.144G	RUN		(	• 😣	
LEDs	Summary	Errors/Alarms	Signal	Histogram	Stop	LEDs	Summar	y Errors/Ala	arms	Signal ł	listogram	Stop
	Graph	Event Log	CPRI RTD	SDT			Graph	Event L	.og	CPRI RTD	SDT	
😑 Signal	Res	sults	Even	t Log	Err inj.	😑 Signal		Results		Event Log		Err inj.
🧿 Frame	ST:2014-05-30 03:5			ET:00/00:00:48	Alarm	😑 Frame	Type Start	Star 14/05/30 03:56:48		Duration [ms]	Verdict	Alarm
Pattern	Last	SDT	[ms] 100.850 14/05/	Start Time 30 03:57:27.780400	Alarm/Err	Pattern	Disruption -CPRI:LOF	14/05/30 03:57:22	7.780400	100.850		Alarm/Err
	Max		100.850 14/05/	30 03:57:27.780400			-LSS	14/05/30 03:57:27	7.780400	0.010		
O ALM/ERR	Min			30 03:57:27.780400		O ALM/ERR	-BIT -CODE	14/05/30 03:57:27 14/05/30 03:57:27		0.010 100.590		
History	Result			Waiting for trigger	LASER Off	History	-LOS -LSS	14/05/30 03:57:27 14/05/30 03:57:27		100.300		LASER Off
	Events			1			-LSS -BIT	14/05/30 03:57:21		0.010 0.010		
RX: 6.144G						RX: 6.144G						
TX: 6.144G					Restart	TX: 6.144G		٩	Page 1 of	1 🕑		Restart
I92.168.0.148	Remote/CLI		201	4-05-30 03:57:36		192.168.0.148	Remote/CLI	I	_	2014-05-3	0 03:57:50	

# Měření zpoždění



CPRI Standard Cable Delay Measurement reference points: Toffset = Frame offset delay between Slave RX and Slave TX T 1,4 = Frame delay between Master TX and Master RX Cable Delay (round trip) = T 1,4 – Toffset

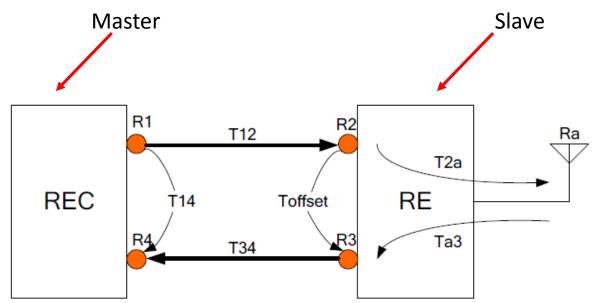
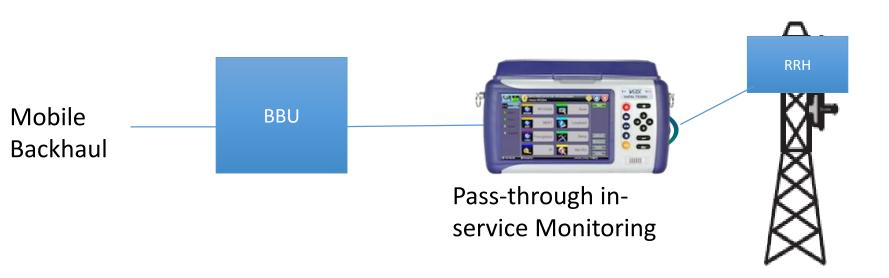




Figure 24: Definition of reference points for delay calibration (single-hop configuration)



#### Monitoring CPRI provozu



- Configured for dual port pass through monitoring or with optical splitters
- Troubleshoot protocol interop with Control words display
- Link status and alarm display
- Hyperframe capture



#### Analýza řídicího protokolu

Control Words Decode for Uplink and												
Image: Second se												
	5.0	3040										
LEDs	0-15		16-31					32-47	48-63			
	0	Sync & timing	SYN	HFN	BFN	BFN	8	Reserved	RES	RES	RES	RES
😑 🕒 Signal			вс	2F	DD	02			00	00	00	00
Joigha	1	Slow C&M	C&M	C&M	C&M	C&M	9	Reserved	RES	RES	RES	RES
0.0-			7E	7E	7E	7E			00	00	00	00
○○ Frame	2	L1 inband prot.	VER	STR	L1	Ptr	10	Reserved	RES	RES	RES	RES
			01	02	00	14			00	00	00	00
O Pattern	3	Reserved	RES	RES	RES	RES	11	Reserved	RES	RES	RES	RES
			00	00	00	00			00	00	00	00
OOALM/ERR	4	Ctrl_AxC low	Ctl	Ctl	Ctl	Ctl	12	Reserved	RES	RES	RES	RES
		Byte	00	00	00	00			00	00	00	00
History	5	Ctrl_AxC low	Ctl	Ctl	Ctl	Ctl	13	Reserved	RES	RES	RES	RES
		Byte	00	00	00	00			00	00	00	00
	6	Ctrl_AxC high	Ctl	Ctl	Ctl	Ctl	14	Reserved	RES	RES	RES	RES
		Byte	00	00	00	00			00	00	00	00
RX: 9.8304G	7	Ctrl_AxC high	Ctl	Ctl	Ctl	Ctl	15	Reserved	RES	RES	RES	RES
TX: Through		Byte	00	00	00	00			00	00	00	00
P 192.168.0.88	R Re	emote/CLI Fa	ictory	Mode				2015-03-13 09:41:58				